

S/N 09/346,283

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Michael R. Flannery

Examiner: Jose R. Diaz

Serial No.: 09/346,283

Group Art Unit: 2815

Filed: July 1, 1999

Docket: 450.202US1

Title: INTEGRATED CIRCUIT WITH UNIFIED INPUT DEVICE,
MICROPROCESSOR AND DISPLAY SYSTEMS



RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

5/ election
1/ Step 1
11/22/00

In response to the Restriction Requirement mailed September 8, 2000, Applicant elects, without traverse, to prosecute the claims of Group I (claims 1-7, drawn to the apparatus). Applicant respectfully cancels remaining claims 8-11 (Group II, drawn to apparatus fabrication) without prejudice, and reserves the right to reintroduce them in a divisional application at a later date.

The Examiner is invited to contact Applicant's Representatives at the below-listed telephone number if there are any questions regarding this Response or if prosecution of this application may be assisted thereby.

RECEIVED
NOV 20 2000
TECHNOLOGY CENTER 2800

Respectfully submitted,
MICHAEL R. FLANNERY
By their Representatives,
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6954

Date Nov 6 2000 By John M. Dahl
John M. Dahl
Reg. No. 44,639

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 7th day of November, 2000.

Name

David B. Brundage

Signature

David B. Brundage